

Ph.D. Program (Spring 2026)

The Department of Electrical Engineering at Shiv Nadar Institution of Eminence Deemed to be University, Delhi NCR, invites applications for admissions to its Ph.D. program.

SPECIALIZATION OFFERED

- Microelectronics and VLSI
- Organic and Flexible Electronics/Materials
- Power Engineering
- Modern Control Systems
- IoT and Embedded Systems
- RF and Microwave
- Signal and Image Processing
- Communication Systems and Networks
- Machine Learning and AI

MINIMUM ELIGIBILITY CRITERIA

- M.Tech./M.E. degree in engineering (minimum 6CPI or 60%)
- B.Tech./B.E. degree in relevant discipline (minimum 7.5 CPI or 75%)
- M.Sc. degree in Physics and allied areas (minimum 6.5 CPI or 65%)
- M.Sc. degree in Electronics (minimum 6.5 CPI or 65%)
- M.Sc. degree in Mathematics (minimum 6.5 CPI or 65%)

Note: Candidates with valid CSIR-JRF / UGC-NET / GATE certificates will be exempted from the written test.



HIGHLIGHTS

- Institute assistantship INR 45,000/- per month for the First Two Years
- Institute assistantship INR 50,000/- per month for the Last Three Years
- Eligible to apply for the Visvesvaraya Fellowship
- Travel support of INR 1,50,000/- for national and international conferences
- Opportunities to work with international universities/exchange programs
- Opportunities to be a part of cutting-edge research

IMPORTANT DATES:

- Last date for receipt of completed application: **Sunday, 30th Nov., 2025**
- Written Test: **Saturday, 6th Dec., 2025**
- Technical Interview: **Wednesday, 17th Dec., 2025**
- Declaration of Results: **Saturday, 20th Dec., 2025**

IMPORTANT LINKS:

Department Webpage

Admission Webpage



(for detailed Syllabus please refer to Annexure A, Annexure B and Annexure C)